Chip back potential is the level which bulk silicon is maintained by on-chip connection, or it is the level to which the chip back must be connected when specifically stated below. If no potential is given the chip back should be isolated.



**Top Material: Al**

**Backside Material: Au**

**Bond Pad Size: .019”**

**Backside Potential: Cathode**

**Mask Ref:**

**APPROVED BY: DK DIE SIZE .037” X .037” DATE: 11/10/21**

**MFG: KNOX/AEROFLEX THICKNESS .008” P/N: 1N5313**

**DG 10.1.2**

#### Rev B, 7/19/02